

DFW



Attorney Docket No.: 69804-015

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
Mitsutoshi FUJITA, et al. : Confirmation Number: 9744
Serial No.: 10/815,690 : Group Art Unit: 2891
Filed: April 2, 2004 : Examiner: CARIDAD EVERHART
For: METHOD OF DESIGNING LOW-POWER SEMICONDUCTOR
INTEGRATED CIRCUIT

RESPONSE UNDER 37 C.F.R. § 1.111

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated June 20, 2005, having a shortened statutory period for response set to expire September 20, 2005, Applicants respectfully request reconsideration of the pending rejection for the reasons set forth below.